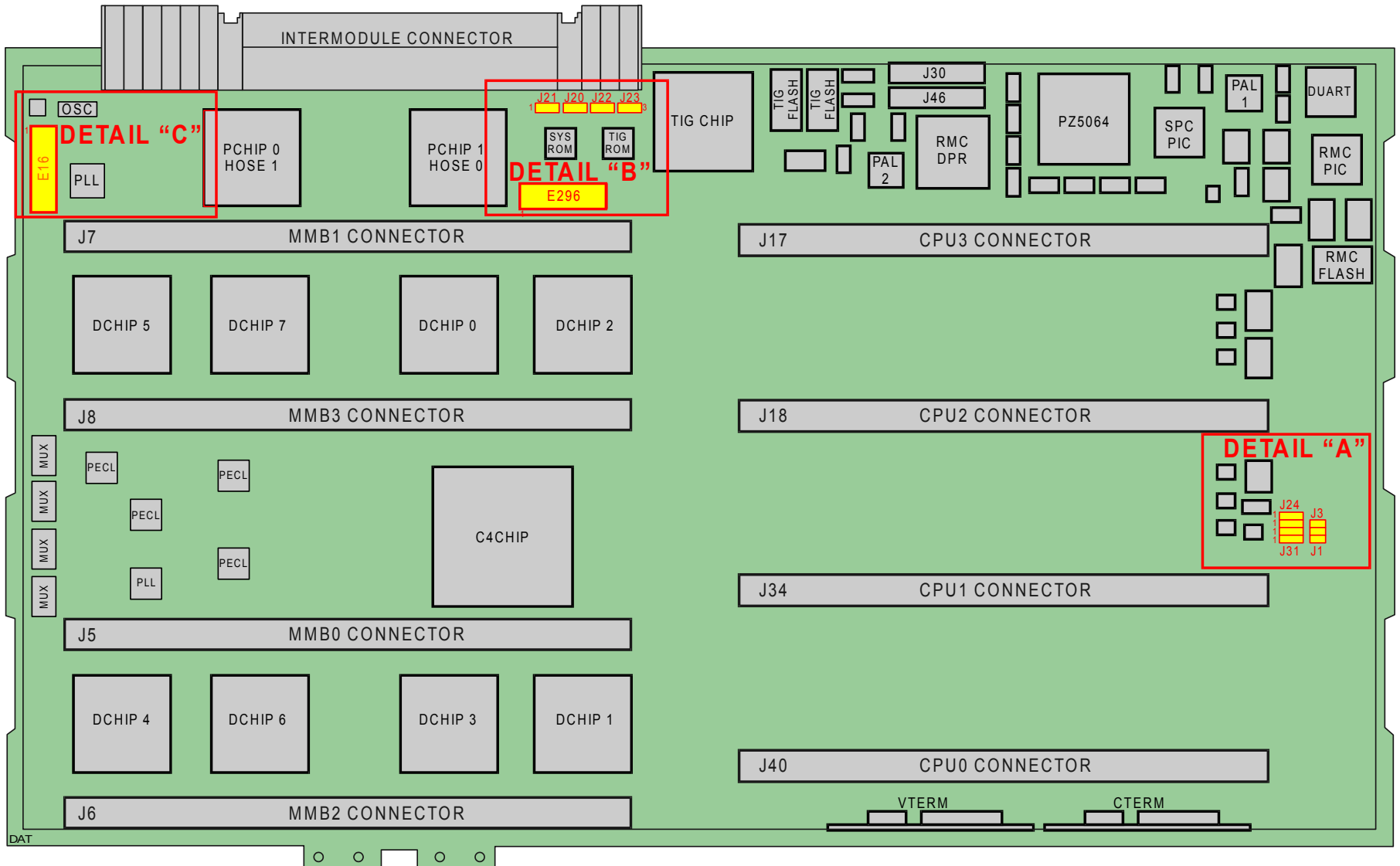




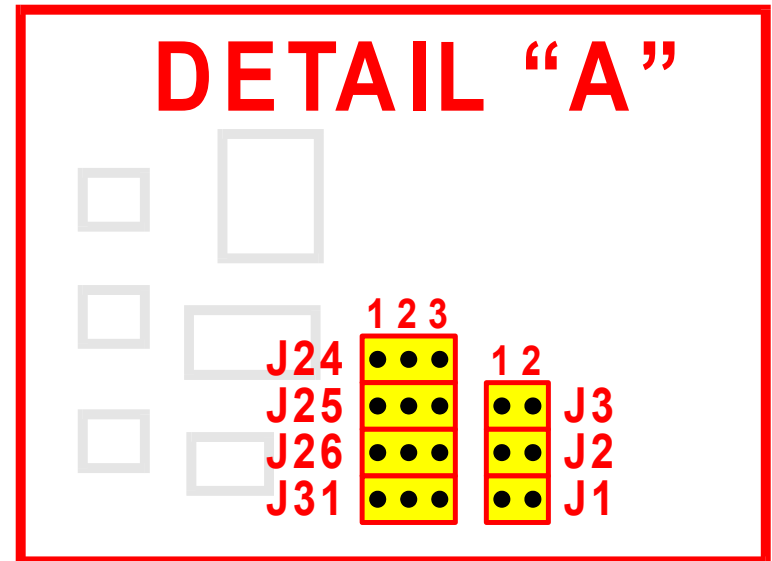
Jumpers and Switches

Dave Tatosian
Alpha Volume Server Engineering

CSB Jumper and Switch Locations

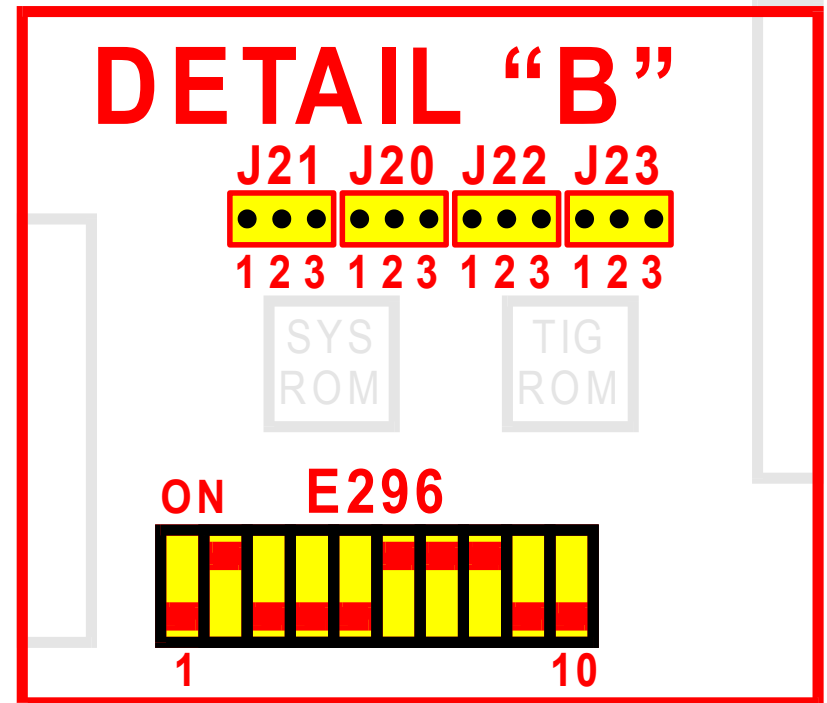


- **CSB J24**
 - 1-2: Disable RMC Flash Update
 - 2-3: Enable RMC Flash Update
- **CSB J25**
 - 1-2: Set RMC Defaults
 - 2-3: Normal Mode
- **CSB J26**
 - 1-2: Enable Temp Fail Shutdown
 - 2-3: Disable Temp Fail Shutdown
- **CSB J31**
 - 1-2: Allow RMC to control COM1 Bypass
 - 2-3: Disable COM1 Bypass
 - NONE: Force COM1 Bypass
- **CSB J1**
 - Install to Bypass Power-up Checks by SPC
- **CSB J2**
 - Reserved (not installed)
- **CSB J3**
 - Reserved (not installed)



TIG/SROM Jumper Settings

- **CSB J21**
 - 1-2: Load TIG from Flash RAM
 - 2-3: Load TIG from Serial ROM
- **CSB J20**
 - FIR_FUNC2
 - 1-2 = "0", 2-3 = "1"
- **CSB J22**
 - FIR_FUNC1
 - 1-2 = "0", 2-3 = "1"
- **CSB J23**
 - FIR_FUNC0
 - 1-2 = "0", 2-3 = "1"

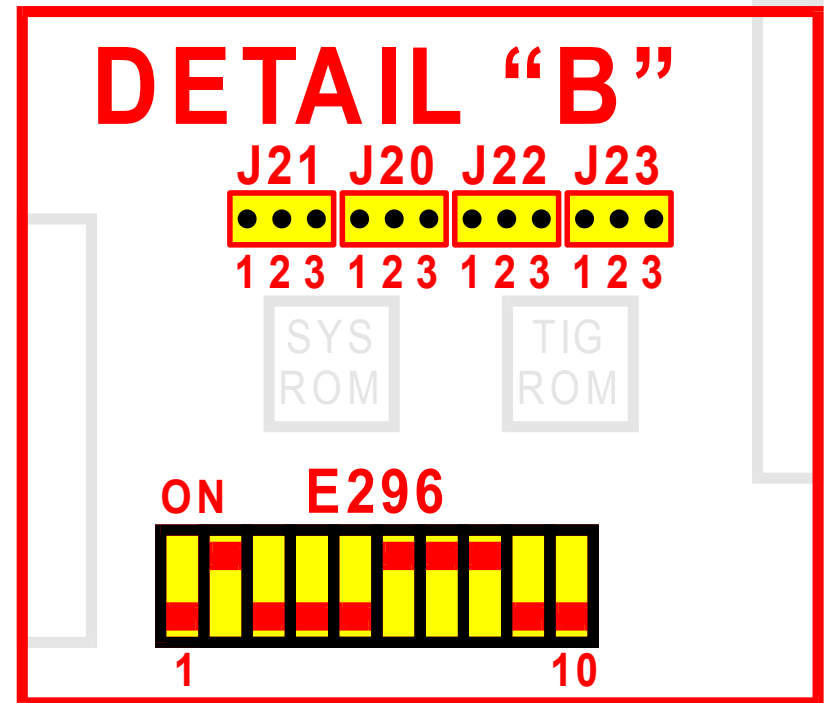


FIR_FUNC Table

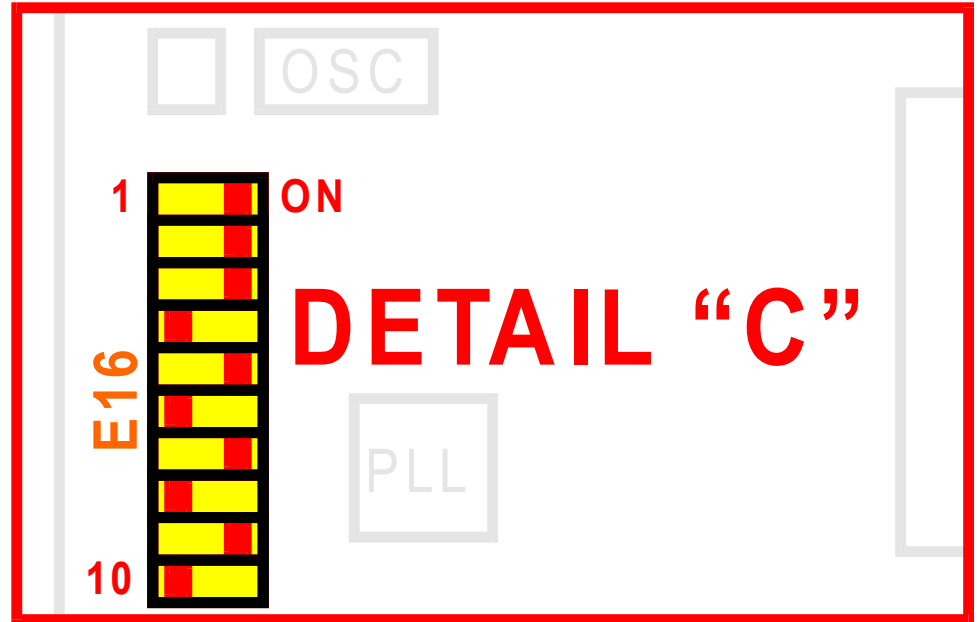
000 = Normal
001 = Prevent FLASH Loads/Load from SROM
010 = Load from Floppy
111 = Lock Console

TIG Switch Settings

- CSB Switchpack E296
 - SW1: SYS_EXT_DELAY1 (off)
 - SW2: SYS_EXT_DELAY0 (on)
 - SW3: SYS_FILL_DELAY (off)
 - SW4: CPU_CFWD_PSET (off)
 - SW5: PCI_CLK_DIV_IN1 (off)
 - SW6: PCI_CLK_DIV_IN0 (on)
 - SW7: Y_DIV3 (on)
 - SW8: Y_DIV2 (on)
 - SW9: Y_DIV1 (off)
 - SW10: Y_DIV0 (off)



- **SW1: M0 (on)**
- **SW2: M1 (on)**
- **SW3: M2 (on)**
- **SW4: M3 (off)**
- **SW5: M4 (on)**
- **SW6: M5 (off)**
- **SW7: M6 (on)**
- **SW8: N0 (off)**
- **SW9: N1 (on)**
- **SW10: XTAL_SEL (off)**



- **Sets the System Board MAIN CLOCK frequency**



- **CPB J13**
 - 1-2: Enable VBAT to RTC Chip
 - 2-3: Disable VBAT to RTC Chip
- **CSB J20**
 - 1-2: Enable PCI 0 PME
 - 2-3: Disable PCI 0 PME
- **CSB J21**
 - 1-2: Enable PCI 1 PME
 - 2-3: Disable PCI 1 PME
- **CSB J31**
 - 1-2: Do Not Force COM1 DTR
 - 2-3: Force COM1 DTR