



HOT-555A Kurzanleitung

(Ausführliches Handbuch auf der Spacewalker-CDROM)

- 1) CPU-Konfiguration wie in den Tabellen angegeben. Spannungsangaben für die CPUs sind unverbindlich, Änderungen durch die CPU-Hersteller vorbehalten.

Beispiel: 4-Pin-Jumper, Pin 3 und 4 geschlossen: 

- 2) Speicher-Bestückung: Die 72pin SIMM-Sockel können paarweise mit gleichen Modulen Fast-Page-Mode oder EDO in den Größen 4, 8, 16 und 32 MB (60 oder 70 ns) bestückt werden. Die 168pin DIMM-Sockel können mit DIMM-Modulen der Größe 8, 16 oder 32 MB (FPM, EDO oder SDRAM) auch einzeln bestückt werden. Es sollten nicht gleichzeitig 5V-SIMM-Module und 3V-DIMM-Module bestückt werden!
- 3) Unmittelbar nach dem Einschalten des Rechners drücken Sie die <ENTF> Taste, um das Bios-Setup-Programm zu starten.

HOT-555A Installation guide

(Complete manual on the Spacewalker-CDROM)

- 1) CPU configuration according to the tables. The voltage instructions of the CPUs may be changed at a later date without prior notification by the manufacturer.

Example: Four-Pin-Jumper:

Pin 3 & 4 are closed by jumper cap. 

- 2) Memory-Configuration: the 72pin sockets can be filled with pairs of same Fast-Page-Mode- and/or EDO-modules in sizes of 4, 8, 16 and 32 MB (60 or 70 ns). The 168pin sockets can be equipped with one or two DIMM-modules in size of 8, 16 or 32 MB (FPM, EDO or SDRAM). Do not populate both 5V SIMM modules and 3,3V DIMM modules at the same time!
- 3) Power on the computer and press immediately will allow you to enter Bios setup program.

CPU Einstellungen / CPU Configuration

Processors	JP37	System Clock / Multiplier	Multiplier JP A / JP B
Pentium/MMX 233 MHz Pentium 100 MHz AMD-K6 PR2-233 AMD-K5 PR100/133		66 MHz x 1.5 / x 3.5	
Pentium/MMX 200 MHz AMD-K6 PR2-200 AMD-K5 PR200		66 MHz x 3	
Pentium/MMX 166 MHz AMD-K6 PR2-166 MHz AMD-K5 PR166 MHz		66 MHz x 2.5	
Pentium 150 MHz AMD-K5 PR150		60 MHz x 2.5	
Pentium 133 MHz Cyrix 6x86/L P166+ IBM 6x86/L P166+		66 MHz x 2	
Pentium 120 MHz Cyrix 6x86/L P150+ IBM 6x86/L P150+		60 MHz x 2	
Cyrix 6x86 PR133+ IBM 6x86 PR133+		55 MHz x 2	
Cyrix 6x86 P120+ IBM 6x86 P120+		50 MHz x 2	
Pentium 90 MHz AMD-K5 PR90/120		60 MHz x 1.5	
Pentium 75 MHz AMD-K5 PR75		50 MHz x 1.5	

Nicht Pin 3 von JPA mit Pin 1 von JPB kurzschließen, sonst werden Mainboard und CPU beschädigt.



Do not short pin 3 of JPA and pin 1 of JPB by a jumper cap, it will cause severe damage to the mainboard and the CPU.

Voltage			CPU Type	Jumper-Setting			
Type	Vcore	Vio		JP39	JP33	JP34	JP43
Single	3,3V	3,3V	Pentium P54C STD Cyrix/IBM 6x86 3,3V				
	3,52V	3,52V	Pentium P54C VRE Cyrix/IBM 6x86 3,52V AMD K5 ABx				
Dual	2,8V	3,3V	Pentium P55C MMX Cyrix/IBM 6x86L				
	2,9V	3,3V	AMD-K6 PR2-166 / PR2-200				
	3,2V	3,3V	AMD-K6 PR2-233				

Weitere Einstellungen / Other Jumper settings		
Vcore JP44	Voreinstellung default	
	erhöht Vcore um 2,5% increases Vcore by 2.5%	
Flash EPROM Voltage JP18	5 Volt (SST)	
	12 Volt (Intel, MX)	

Alle auf diesem Blatt aufgeführten Marken- und Produktnamen sind eingetragene Waren- oder Produktzeichen der jeweiligen Besitzer. Druckfehler, Irrtümer, Innovationen und technische Änderungen vorbehalten.

All brand and product names referred to in this sheet are registered trademarks of their respective holders. All information, documentation, and specifications contained in this guide are subject to change without prior notification by the manufacturer. The author assumes no responsibility for any errors or omissions.

HOT-555A Specification

CPU Function

- ❑ Pentium processors P54C : 75 ~ 200 MHz
- ❑ Pentium processors P55C (MMX) : 166 ~ 233 MHz
- ❑ Cyrix/IBM 6x86/L processors : P120+ ~ P166+
- ❑ AMD K5 processors : PR75 ~ PR200
- ❑ AMD K6 processors : PR2-166 ~ PR2-233

Chipset

- ❑ Intel PCIsset 82437VX, 82438VX and 82371SB

Memory

- ❑ Provides four 72-pin SIMM sockets for 5V EDO and Fast Page Mode SIMMs. Supports 4MB, 8MB, 16MB, 32MB 72-pin SIMMs ranging from 8MB to 128MB.
- ❑ Provides two 168-pin DIMM sockets for 3.3V Sync. DRAM, EDO or Fast Page Mode DIMMs. Supports 8MB, 16MB, 32MB and more 168-pin DIMMs.

Cache Memory

- ❑ Integrated L2 write-back cache controller
 - 256kB or 512kB (option) direct Mapped Pipeline Burst Cache

Power Management Function

- ❑ Provides four power management modes : Full on, Doze, Standby, and Suspend
- ❑ Supports Microsoft APM
- ❑ Provides EPMI (External Power Management Interrupt) pin

Expansions

- ❑ 32-bit PCI bus slot x 4
- ❑ 16-bit ISA bus slot x 3
- ❑ 2-channel PCI IDE port
 - Support up to 4 IDE devices
 - PIO Mode 4, DMA Mode 2 transfers up to 22 MB/sec
 - Integrated 8 x 32-bit buffer for PCI IDE burst transfers
- ❑ One floppy port
- ❑ One parallel port
 - Supports **SPP** (PS/2 compatible bidirectional Parallel Port), **EPP** (Enhanced Parallel Port), and **ECP** (Extended Capabilities Port) high performance parallel port.
- ❑ Two serial ports
 - Supports 16C550 compatible UARTS.
 - Supports IrDA (Infra-red) communication.
- ❑ One PS/2 mouse port
- ❑ Two USB (Universal Serial Bus) ports

System Bios

- ❑ Award PnP Bios v4.51PG
 - Bundled with Symbios Login (NCR) SDCM V4.0 SCSI Bios

Board Design

- ❑ AT form factor, Dimension 220mm x 230mm

