



DPSD BIOS ENGINEERING

Intel Corporation, 5200 NE Elam Young Parkway
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DATE: July 17, 2003
PRODUCT: Intel® D865PERL Desktop Board Standard BIOS

P11-0066 (Production 11, build 0066)

About This Release:

- 09-18-2003
- Core Baseline version 01.05.01
- RL86510A.86A.0066.P11.0309181611
- UNDI 4.1.10
- Tanacross UNDI 1.2.10

New Fixes/Features:

- Added IDCC Memory Extended Configuration information.
- Fixed the Blue Screen issue seen after a certain amounts of OS resets.
- Updated 865G/865PE Memory reference code to version 1.4.
- Updated processor branding info.
- Changed display of DDR333 to DDR320 when used with a 800 Mhz FSB processor.
- Added IDCC fan information.
- Added enhancement to flash update algorithm by validating BIOS image and check for memory stability.
- Corrected language translation issues.

P09-0061 (Production 09, build 0061)

About This Release:

- 08-28-2003
- Core Baseline version 01.05.01
- RL86510A.86A.0061.P09.0308281850
- UNDI 4.1.10
- Tanacross UNDI 1.2.10

New Fixes/Features:

- Added latest processor support.
- Fixed issue where if IFLASH issue that would cause system to hang at POST code 55h.
- Fix issue where system will POST hang at 00h after setting manufacturing jumper due to VID been cleared to 00h.
- Added support for multiple capsules and split/fragmented capsules
- Fixed issue where event log fails to clear on a reboot using a CMOS utility
- Added support for future processors.
- Added limited IDCC support.
- Fixed a system hang problem during POST with a Flash USB Flash card reader/writer.

P08-0058 (Production 08, build 0058)

About This Release:

* Other names and brands may be claimed as the property of others.

- Core Baseline version 01.05.01
- RL86510A.86A.0058.P08.0307151556
- UNDI 4.1.10
- LAN UNDI 1.2.10

New Fixes/Features:

- Updated Intel(R) RAID Option ROM to version v3.5.0.2568.
- Enhanced flash update algorithm to fix post code E9 hang issue.
- Intel® 865G\PE\GV and Intel® 848P (Springdale) MCH BIOS Specification Update v1.4 & Memory Initialization Reference Code v1.3.
- Updated System Configuration Data.
- Added ASF Support
- Added codes to remove Intel(R) RAID Technology setup option if IDE is not in Enhanced mode.
- Added work-around for IRQ sharing issue with a 3rd party drive image application in SATA native mode.
- Added the ability to enable center spread clocking via BIOS set up option.
- Added functionality to flash update the BIOS without having to reboot
- Added error handling, Flash5 failed message during POST
- Added increasing scale of beep tones when flash update is in progress.
- Added a workaround for a synthetic memory test utility that hangs when USB legacy support is enabled.
- Fixed an issue that caused a delay when booting to a floppy after attempting to boot to a non-bootable CD with two SATA drives attached.
- Fixed an issue where the Event Log could not be cleared by setting a CMOS token.

P07-0056 (Production 07, build 0056)

About This Release:

- Core Baseline version 01.03.00
- RL86510A.86A.0056.P07.0305201221
- UNDI 4.1.10
- LAN UNDI 1.2.10

New Fixes/Features:

- Added CORE support for manual PCI IRQ routing.
- Lowered power consumption of Gigabit LAN when system is in S5.
- Fixed the issue, where some boards hanging at POST code 20h when CMOS battery is re-installed or at first power-up.
- Fixed boot block recovery failure during BIOS flash update with HT processor.
- Fixed issue where Windows XP legacy-to-native IDE runtime mode switching was not functioning.
- Added CKE workaround for enhanced S3 robustness.
- Correct fan monitoring text and fan speed RPM display.
- Added Audio progress indicator for BIOS updates. An increasing scale of beep tones are now emitted during BIOS updates.

P06-0052 (Production 06, build 0052)

About This Release:

- 05-02-2003
- Core Baseline version 01.03.00
- RL86510A.86A.0052.P06.0305020834
- UNDI 4.1.10

- Intel® 82547EI UNDI 1.2.10

New Fixes/Features:

- Fixed BIOS Hardware Health Monitor fan speed display.
- Capitalized RAID in the RAID setup option.
- Add "Additional System Information" in BIOS Setup Menu.
- Added Grayscale JPEG image support. Also added more robust error handling in JPEG decoder.
- Added processor support.
- Fix for the Backup block string being intact when the power fails during the middle of an erase cycle.
- Fixed boot to USB Zip media when USB Zip was emulated as Floppy and the actual format of Zip media was HDD.
- Added codes to hide Intel(R) RAID Technology setup option if ICH5-R is not present.

P05-0050 (Production 05, build 0050)

About This Release:

- 04-22-2003
- Core Baseline version 01.03.00
- RL86510A.86A.0048.P04.0304091908
- UNDI 4.1.10
- Intel® 82547EI UNDI 1.2.10

New Fixes/Features:

- Banner change to P05-0050
- Updated BIOS support code for Intel® Pentium® 4 processor with 800MHz system bus.
- Updated DPSD small Logo.
- Fixed false memory test post time error message when no failures and when dual channel optimization message needs to appear.
- Fixed WOL from S5 issue with Intel® 82547EI LAN.
- Added support for SKU with Intel Flex-6 audio and 10/100 Mbit/sec LAN subsystem.
- Fixed intermittent hang at post 2A issue and fixed LAN disable issue on some SKUs.
- Added support for Express BIOS Update 2.0.
- Fixed the problem where runtime GPNV Logo enable/disable feature.
- Added Adobe* Photoshop* JPEG image support and Microsoft* Windows* XP sample JPEG image support.
- Fixed issue where when the floppy disabled in Setup the system still booted to the floppy after exiting/escaping the BBS Popup Menu.
- Corrected ACPI S4 Sleep Type register value.
- Updated new System Configuration data

P04-0048 (Production 04, build 0048)

About This Release:

- 04-10-2003
- Core Baseline version 01.03.00
- RL86510A.86A.0048.P04.0304091908
- UNDI 4.1.10
- Intel® 82547EI UNDI 1.2.10

New Fixes/Features:

- Initial Production Release